

## **Gary Tanner**

*Advisor*

Gary currently serves on the Board of Directors of MagnaChip, one to the world's leading foundries, in a role held since August 2015, and was previously its Chairman of the Board from September 2016 to November 2018. He was a member of the Board at Stats Chip Pac from July 2012 to August 2015 - billed as one of the top five Outsourced Semiconductor Assembly and Test (OSAT) globally.

Based out of USA, his extensive industry experience span managing operations of both fabless and OEM companies, personally overseeing multiple wafer fabrication and assembly test plants in USA and Asia and was responsible for the construction, planning and operation of nine fabs.



Delivering strong results in key business metrics through senior executive roles including Chief Operating Officer at International Rectifier (IRF), a top US-based power IDM company; Chief Executive Officer at Zarlink Semiconductor, a Fabless product company; and as a Senior Executive at Intel, National Semi and Texas Instruments, Gary is recognised by industry peers for his capabilities in balancing market dynamics and industry cyclicalities with operational efficiencies.

He has led businesses to deliver record yields, improve cycle times, enhance cost reduction, achieve 100% OTD and raised gross margins from approximately 27% to 54% primarily through manufacturing cost reductions.

Gary graduated with a Bachelor of Science in Technical Management from Regis University and holds a Certificate in Leading Learning Organizations from MIT Organizational Learning Center.

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